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L Number	Hits	Search Text	DB	Time stamp
1	0	6772252.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 07:45
2	283	(710/3.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 08:31
3	790	(710/8.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 08:40
4	397	(710/15.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 08:52
5	133	(710/17.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 08:53
6	215	(710/19.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 08:56
7	89	(710/46.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 09:01
12	575	(710/104.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 09:46
13	84	(710/109.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 09:48
14	270	(710/302.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 10:23

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16	350	(710/301.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 10:25
17	995	(713/1.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 10:24
18	834	(710/305.ccls.) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 10:25
-	6	US-5056001-\$.DID. OR US-5263148-\$.DID. OR US-6295566-\$.DID.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/30 10:09

-	185	((find locate identify) with ((open free empty) adj slot)) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/29 08:36
-	32	((open free empty) adj slot) with (pci "peripheral component interconnect")) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/29 10:16
-	2	6134621.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/29 10:14
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-	11	((pci "peripheral component interconnect") adj device) with present) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/29 13:50
-	1	(pci with slot with device with compare) and @ad<20010131	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/07/29 16:09

50

PLUS Search Results for S/N 09773850, Searched July 30, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

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09773850\_CLS  
Most Frequently Occurring Classifications of Patents Returned  
From A Search of 09773850 on July 30, 2004

Original Classifications

8	710/302
4	710/104
4	710/301
2	361/686
2	710/305
2	710/314
2	710/8
2	713/400

Cross-Reference Classifications

5	710/104
5	710/8
5	713/1
4	710/100
4	710/301
4	710/313
3	710/10
2	361/686
2	361/785
2	361/801
2	709/220
2	709/248
2	710/2
2	710/300
2	713/100
2	713/500
2	713/501

Combined Classifications

9	710/104
9	710/302
8	710/301
7	710/8
5	713/1
4	361/686
4	710/100
4	710/313
3	710/10
3	710/305
3	710/314
2	361/759
2	361/785
2	361/801

09773850\_CLS

2 709/220  
2 709/248  
2 710/2  
2 710/300  
2 710/9  
2 713/100  
2 713/400  
2 713/500  
2 713/501

09773850\_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 09773850 on July 30, 2004

- 9 710/104 (4 OR, 5 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/104 .System configuring
- 9 710/302 (8 OR, 1 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/300 .Bus expansion or extension  
710/301 ..Card insertion  
710/302 ...Hot insertion
- 8 710/301 (4 OR, 4 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/300 .Bus expansion or extension  
710/301 ..Card insertion
- 7 710/8 (2 OR, 5 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/1 INPUT/OUTPUT DATA PROCESSING  
710/8 .Peripheral configuration
- 5 713/1 (0 OR, 5 XR)  
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: SUPPORT  
713/1 DIGITAL DATA PROCESSING SYSTEM INITIALIZATION  
OR CONFIGURATION (E.G., INITIALIZING, SET U  
P,  
CONFIGURATION, OR RESETTING)
- 4 361/686 (2 OR, 2 XR)  
Class 361 : ELECTRICITY: ELECTRICAL SYSTEMS AND DEVICES  
361/600 HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE  
ELECTRICAL COMPONENTS

09773850 CLSTITLES

361/679 .For electronic systems and devices  
 361/683 ..Computer related support  
 361/686 ...Input/output device support

- 4 710/100 (0 OR, 4 XR)  
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
 PROCESSING SYSTEMS: INPUT/OUTPUT  
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
 TRANSACTION PROCESSING)
- 4 710/313 (0 OR, 4 XR)  
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
 PROCESSING SYSTEMS: INPUT/OUTPUT  
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
 TRANSACTION PROCESSING)  
 710/305 .Bus interface architecture  
 710/306 ..Bus bridge  
 710/313 ...Peripheral bus coupling (e.g., PCI, USB,  
 ISA, and etc.)
- 3 710/10 (0 OR, 3 XR)  
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
 PROCESSING SYSTEMS: INPUT/OUTPUT  
 710/1 INPUT/OUTPUT DATA PROCESSING  
 710/8 .Peripheral configuration  
 710/10 ..Configuration initialization
- 3 710/305 (2 OR, 1 XR)  
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
 PROCESSING SYSTEMS: INPUT/OUTPUT  
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
 TRANSACTION PROCESSING)  
 710/305 .Bus interface architecture
- 3 710/314 (2 OR, 1 XR)  
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
 PROCESSING SYSTEMS: INPUT/OUTPUT  
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
 TRANSACTION PROCESSING)  
 710/305 .Bus interface architecture  
 710/306 ..Bus bridge  
 710/314 ...Common protocol (e.g., PCI to PCI)
- 2 361/759 (1 OR, 1 XR)  
 Class 361 : ELECTRICITY: ELECTRICAL SYSTEMS AND DEVICES  
 361/600 HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE  
 ELECTRICAL COMPONENTS

09773850\_CLSTITLES

361/679 .For electronic systems and devices  
 361/748 ..Printed circuit board  
 361/752 ...With housing or chassis  
 361/759 ....With lock or interlock

2 361/785 (0 OR, 2 XR)

Class 361 : ELECTRICITY: ELECTRICAL SYSTEMS AND DEVICES

361/600 HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE  
 ELECTRICAL COMPONENTS  
 361/679 .For electronic systems and devices  
 361/748 ..Printed circuit board  
 361/784 ...Plural  
 361/785 ....With separable connector or socket means

2 361/801 (0 OR, 2 XR)

Class 361 : ELECTRICITY: ELECTRICAL SYSTEMS AND DEVICES

361/600 HOUSING OR MOUNTING ASSEMBLIES WITH DIVERSE  
 ELECTRICAL COMPONENTS  
 361/679 .For electronic systems and devices  
 361/748 ..Printed circuit board  
 361/784 ...Plural  
 361/796 ....With housing or chassis  
 361/801 .....Specific latching or retaining device

2 709/220 (0 OR, 2 XR)

Class 709 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
 SYSTEMS: MULTIPLE COMPUTER OR PROCESS COO

RDINATING

709/200 MULTICOMPUTER DATA TRANSFERRING  
 709/220 .Network computer configuring

2 709/248 (0 OR, 2 XR)

Class 709 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
 SYSTEMS: MULTIPLE COMPUTER OR PROCESS COO

RDINATING

709/200 MULTICOMPUTER DATA TRANSFERRING  
 709/248 .Multicomputer synchronizing

2 710/2 (0 OR, 2 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
 PROCESSING SYSTEMS: INPUT/OUTPUT

710/1 INPUT/OUTPUT DATA PROCESSING  
 710/2 .Input/Output expansion

2 710/300 (0 OR, 2 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA

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710/100 PROCESSING SYSTEMS: INPUT/OUTPUT  
INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/300 .Bus expansion or extension

2 710/9 (1 OR, 1 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/1 INPUT/OUTPUT DATA PROCESSING  
710/8 .Peripheral configuration  
710/9 ..Address assignment

2 713/100 (0 OR, 2 XR)  
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: SUPPORT  
713/100 RECONFIGURATION (E.G., CHANGING SYSTEM SETTING  
)

2 713/400 (2 OR, 0 XR)  
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: SUPPORT  
713/400 SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS,  
DATA, OR PULSES

2 713/500 (0 OR, 2 XR)  
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: SUPPORT  
713/500 CLOCK, PULSE, OR TIMING SIGNAL GENERATION OR  
ANALYSIS

2 713/501 (0 OR, 2 XR)  
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: SUPPORT  
713/500 CLOCK, PULSE, OR TIMING SIGNAL GENERATION OR  
ANALYSIS  
713/501 .Multiple or variable intervals or frequencies